

L Number	Hits	Search Text	DB	Time stamp
-	77748	((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)	USPAT; EPO; JPO; DERWENT	2003/11/12 09:50
-	11	((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:13
-	2	((binary near2 decision near2 daigram) or BDD) and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:42
-	0	canonic and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:14
-	1	canonic\$2 and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:15
-	2	boolean and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:15
-	2	((decision near2 daigram) or BDD) and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; EPO; JPO; DERWENT	2003/11/12 12:43
-	0	((multiple adj1 functions) or (set near2 functions)) and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/12 12:46
-	1	((multiple or set or setting or collection or group or assortment) near2 functions) and ((candidate adj3 cluster) and (((integrated near1 circuit) or IC or VLSI or LSI) same (design\$3 or simulat\$3 or model\$4 or layout)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/11/12 12:49